**FPGA BASED SYSTEM DESIGN**

**QUIZ NO. 01**

**NAME: ROLL NO.**

Attempt all questions.

1. **What is the purpose of DCM? (2)**
2. **Define BRAM and in how many modes we can describe it? (3)**
3. **How many LUTs are in each CLB? (1)**
4. **How many slices has one CLB? Briefly explain the structure of SLICE? (3)**
5. **FPGA has \_\_\_\_\_\_\_ bits of multiplier. (1)**
6. **18 bits (b) 16 bits (c) 32 bits (d) 36 bits**